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Application Number:10/736,386 Filing date:December 15, 2003 First named inventor:Whitefield, Bruce Attorney docket number:03-1345 Transmitted herewith for filing via facsimile: • Transmittal Form, PTO/SB/21	REMARKS:	For your review	☐ Reply ASAP	☐ Please comment
-	Filing date:De First named inventor:W	ecember 15, 2003 hitefield, Bruce	· · · · · · · · · · · · · · · · · · ·	
Information Disclosure Statement (letter) Information Disclosure Statement by Applicant PTO/SB/O8A	Transmittal Form, PTO/S Information Disclosure S	B/21 tatement (letter)	/SB/O8A	

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PYO/SB/21 (09-04) Approved for use through 07/31/2008, OMB 0851-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE offection of information unless it displays a valid OMB control number. Under the Paperwork Reduction Act of 1995, no persons are required to respond to Application Number 10/736,386 Filing Date TRANSMITTAL December 15, 2003 First Named Inventor Whitefield, Bruce FORM Art Unit Examiner Name N/Α (to be used for all correspondence after initial filing) Attorney Docket Number 03-1345 Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Drawing(s) Fee Transmittal Form Appeal Communication to Board Licensing-related Papers of Appeals and Interferences Fee Attached Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Petition Amendment/Reply Petition to Convert to a Proprietary Information Provisional Application After Final Power of Attorney, Revocation Status Letter Change of Correspondence Address Affidavits/declaration(s) Other Enclosure(s) (please Identify Terminal Disclaimer below): Extension of Time Request Request for Refund Express Abandonment Request CD. Number of CD(s) Information Disclosure Statement Landscape Table on CD Certified Copy of Priority Remarks Document(s) - IDS Filing Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1,52 or 1.63 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name LSI Logic Corporation Signature Printed name Timothy R. Croll Reg. No. Date 36,771 JO 482 CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below: Signature

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commence, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS, SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bruce Whitefield et. al.

Serial No.:

10/736,386

Filed:

December 15, 2003

For:

Method For Calculating High-

Resolution Wafer Parameter Profiles

Group Art Unit:

Examiner:

Atty Docket:

/ 03-1345

2123

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INFORMATION DISCLOSURE STATEMENT

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Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

9JANOG

Respectfully submitted.

Timothy Croll

Reg. No. 36,771

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STA	EMENT	BY APPI	LICANI	First Named Inventor	Bruce Whitefield
				Group Art Unit	2123
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	/ 03-1345

		U.S. PATENT DOC	UMENTS	
Examiner Cite Initials No.	U.S. Patent Document Number Kind Code (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	6,775,630	Behkami et al.	08-10-2004	
	6,477,685	Lovelace	11-05-2002	
i	2002/01219	Alonso Montull et al.	09-05-2002	
	6,393,602	Atchinson et al.	05-21-2002	
	6,185,707	Smith et al.	02-06-2001	
:	6,067,507	Beffa	05-23-2000	
	5,982,920	Tobin et al.	11-09-1999	
	5,787,190	Peng ct al.	07-28-1998	

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /H.J./ (02/12/2008)

Examiner	/Hugh Jones/ (02/12/2008)	Date
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